

FIG. 1

"IDEAL" SYSTEM I & Q
COMPONENTS ARE
CORRECTLY DETECTED
GIVING CORRECT SYMBOL

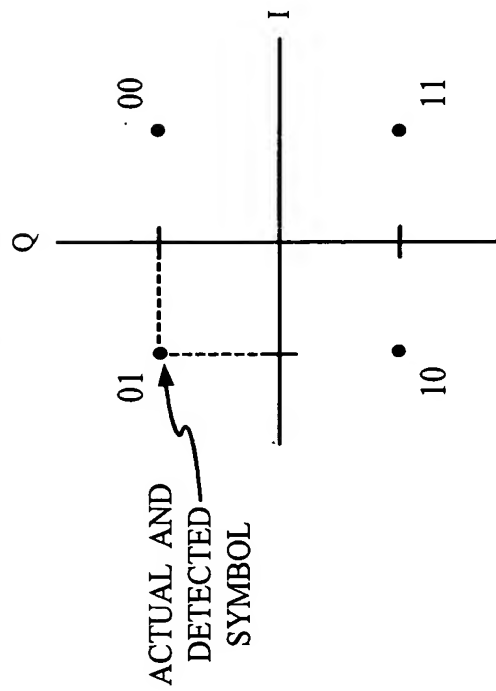


FIG. 2A

"ACTUAL" SYSTEM WITH DC
OFFSETS ON I, Q OR BOTH
COMPONENTS POSSIBLY
CAUSING ERROR

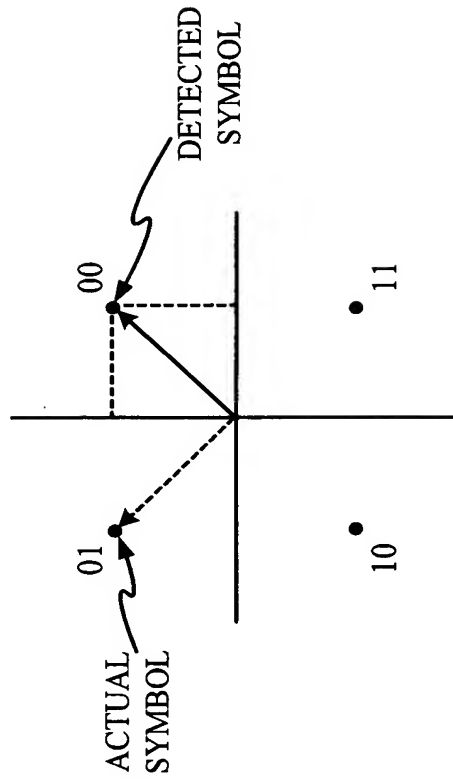


FIG. 2B

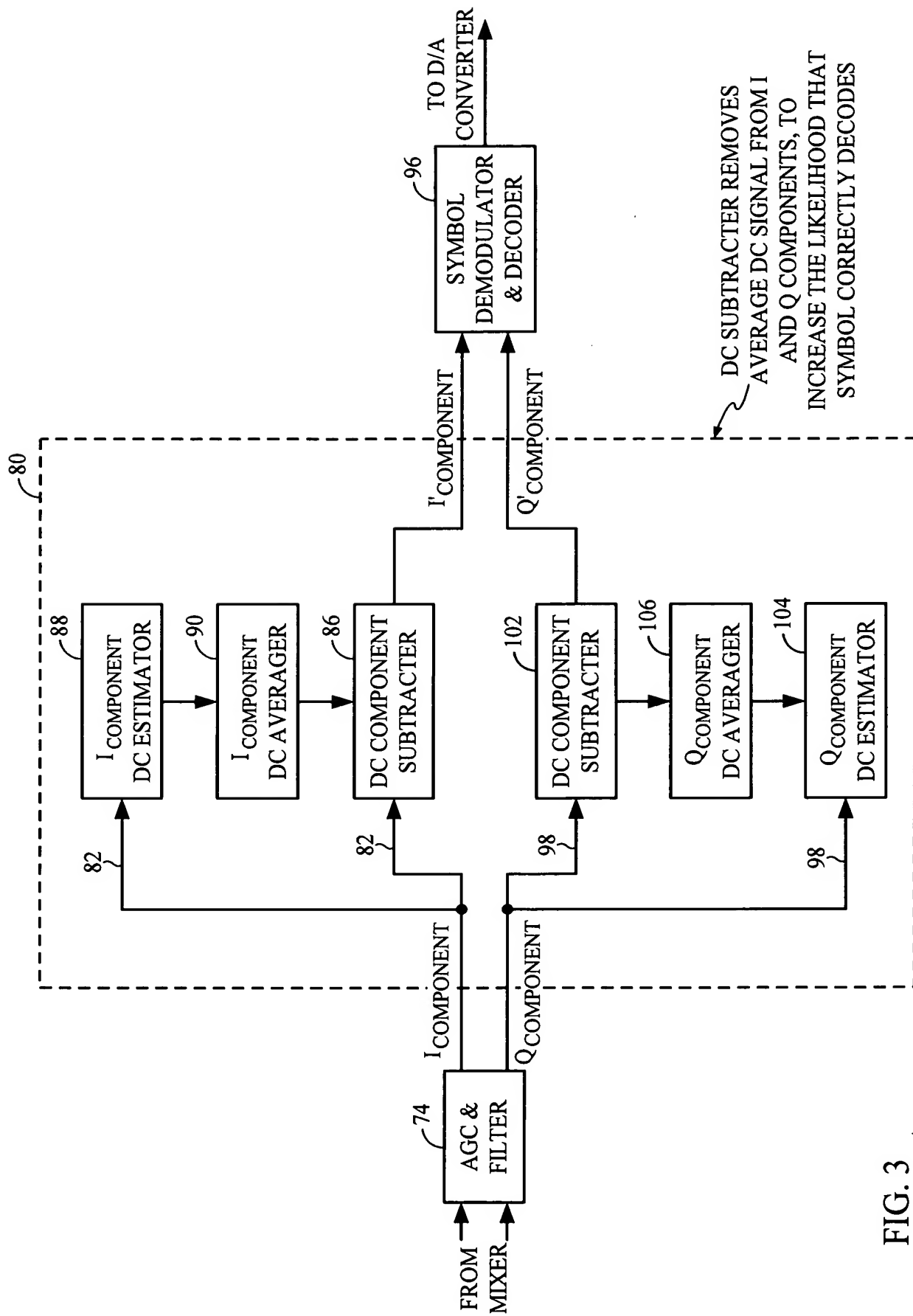


FIG. 3